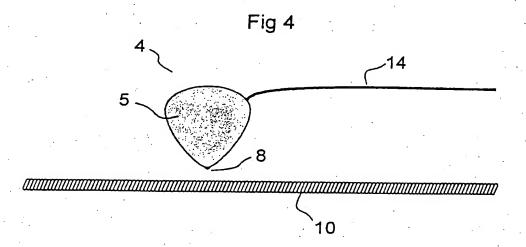
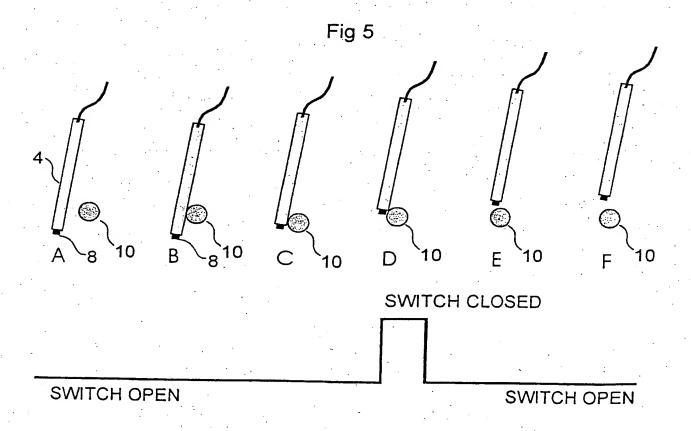


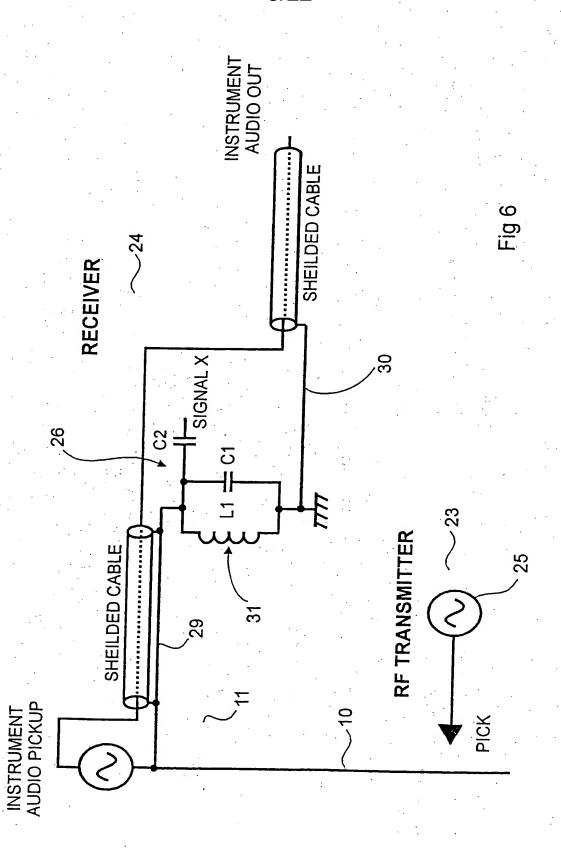
Substitute Sheet (Rule 26) RO/AU

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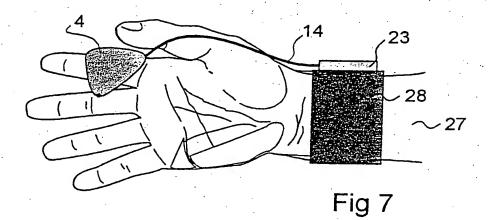
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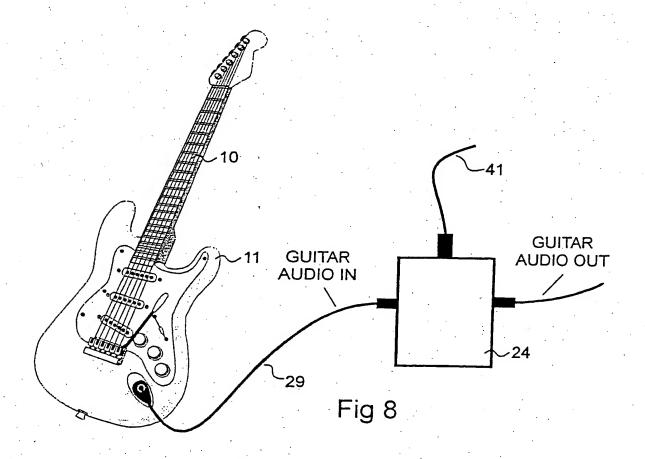




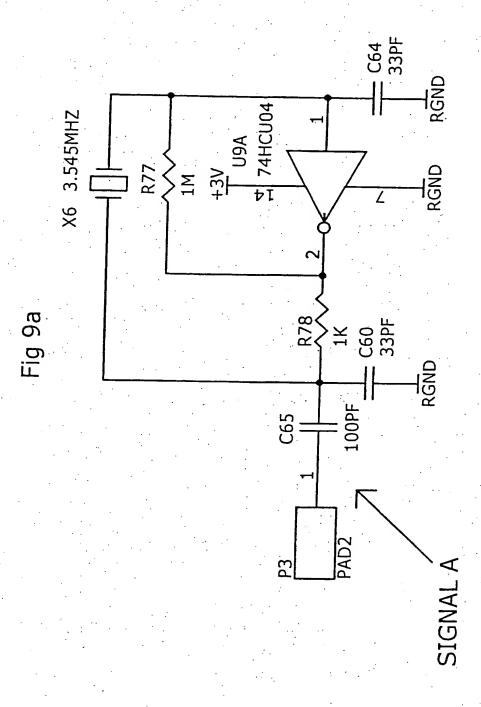


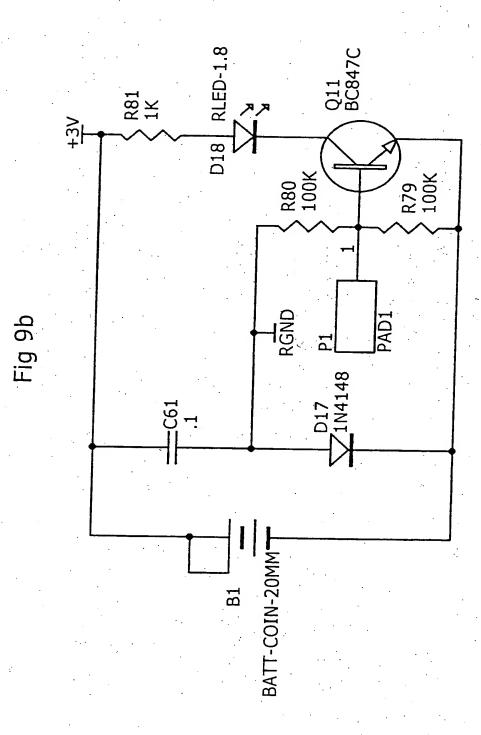
Substitute Sheet (Rule 26) RO/AU





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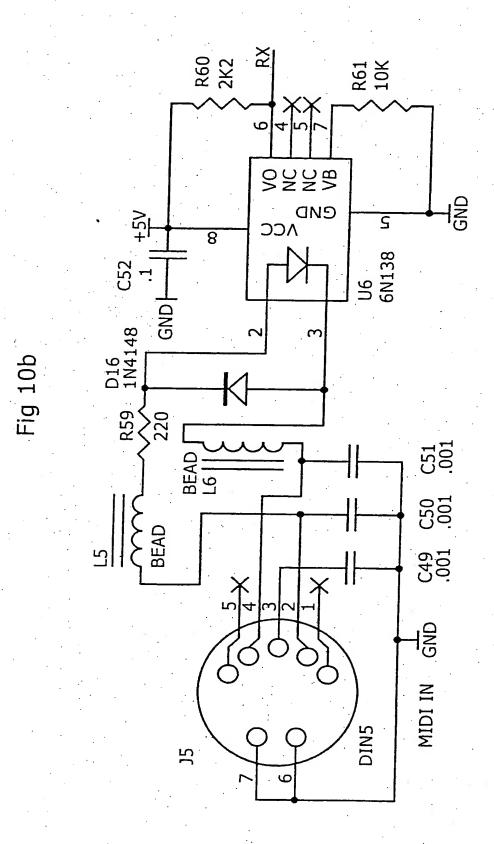




Substitute Sheet (Rule 26) RO/AU

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7/22 D8 1N4148 39 GND 33PF GND _C18 33PF C17 R12 1M LOR25 47K C16 왕 GND CMPB R11 |+5V MC68HC705C8A 4MHZ Ξ 39 38 RESET IRQ VPP TCAP TCAP PD7 PD5 PD3 PD3 PD1 ဆ **0SC2** ÓSC1 +50 $\Lambda D\overline{D}$ SSA 01 100K 100K 100K 100K PA1 PA2 PA3 PA4 PA5 PB1 PB1 PB1 PB2 PC2 PC3 PC3 PC5 PC5 +5V 6 ω **R9** ED2 ED3 GAT **R8** TLC7528 R7 D0 D1 D2 D3 D4 D6 C19 R5\R6 **PEND** DEND +57 S ACC ,R4 OUT-A RFB-A VREF-A A/B A/B /WR OUT-B RFB-B S DA1 SI \R3\ PULL 8 S1 SW-DIP-10 +50 100K 100K 100K 1.5VDA0 R10 10K 4∕B DA1 10a 860 R1 10K Substitute Sheet (Rule 26) RO/AU



Substitute Sheet (Rule 26) RO/AU

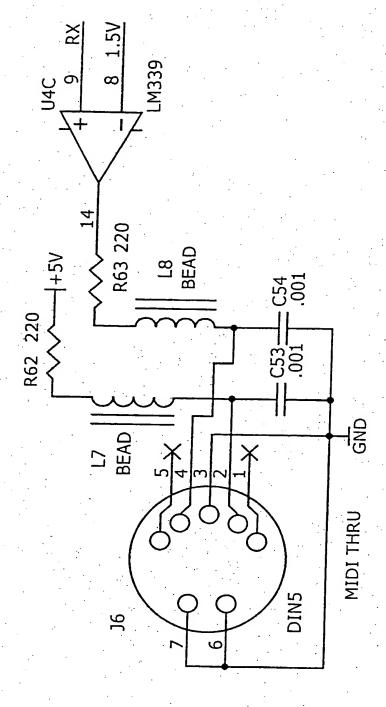


Fig 10c

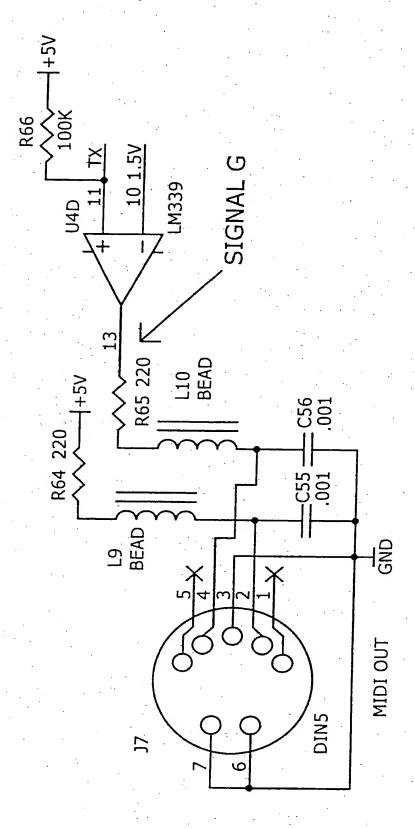
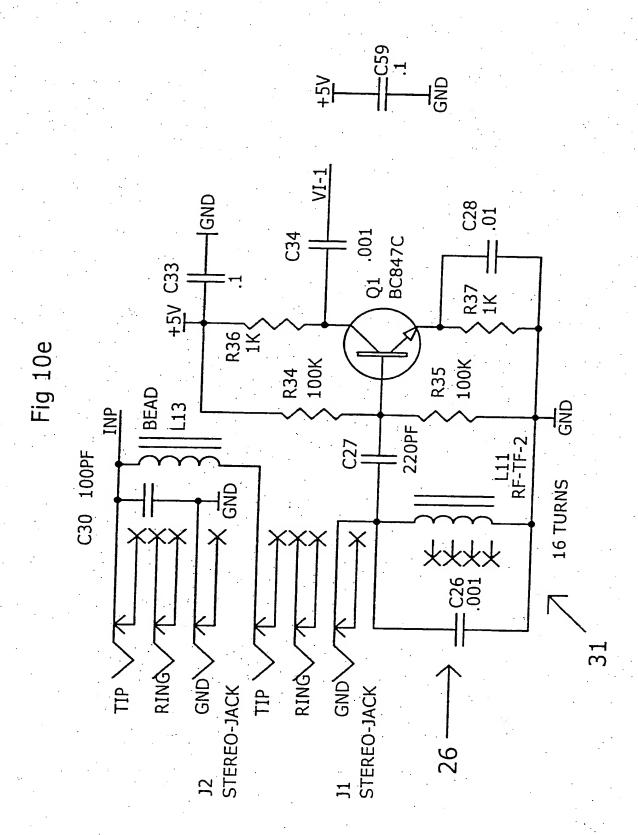


Fig 10d

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Substitute Sheet (Rule 26) RO/AU

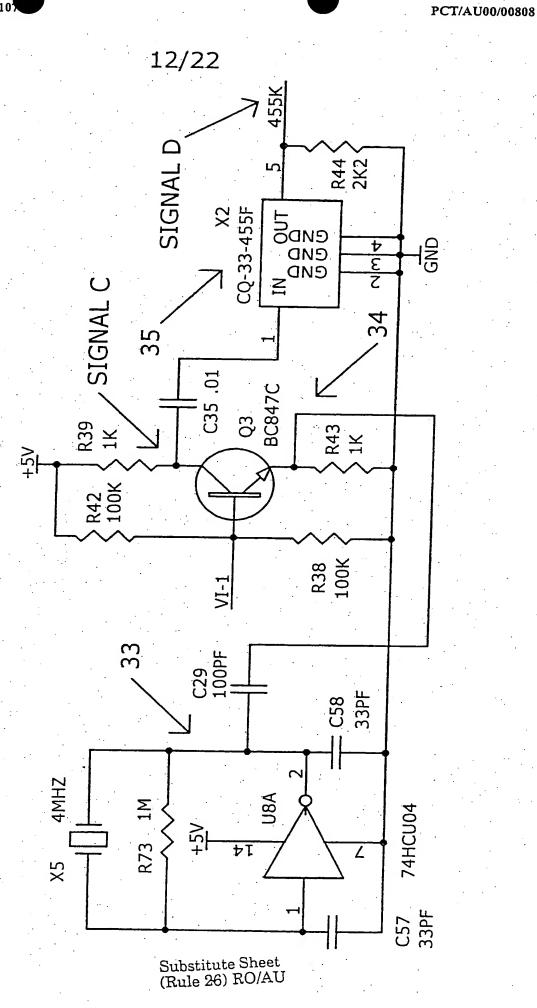
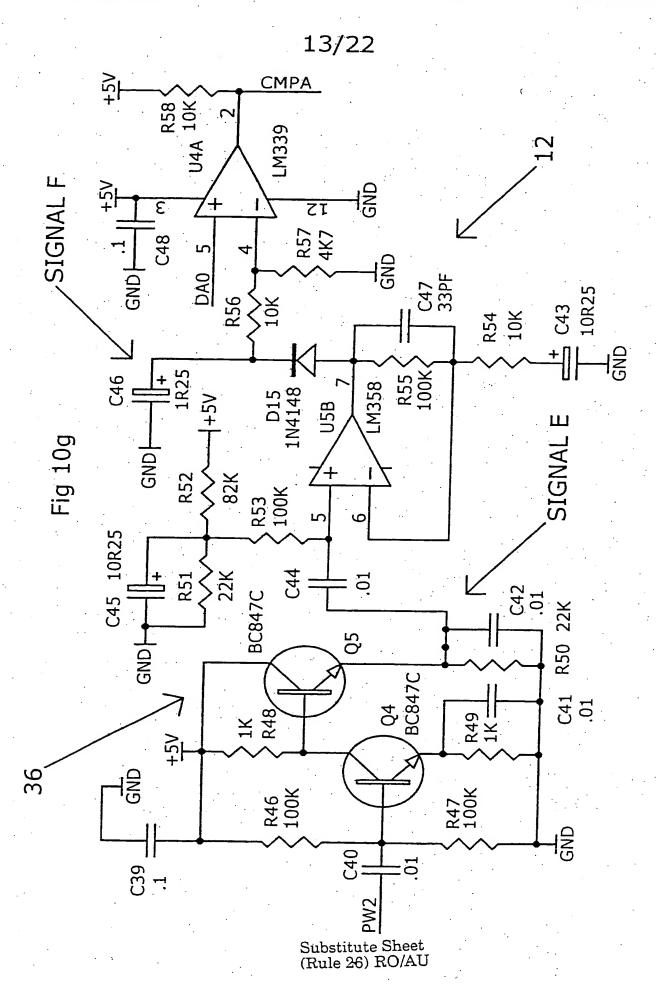


Fig 10f

(3.3)

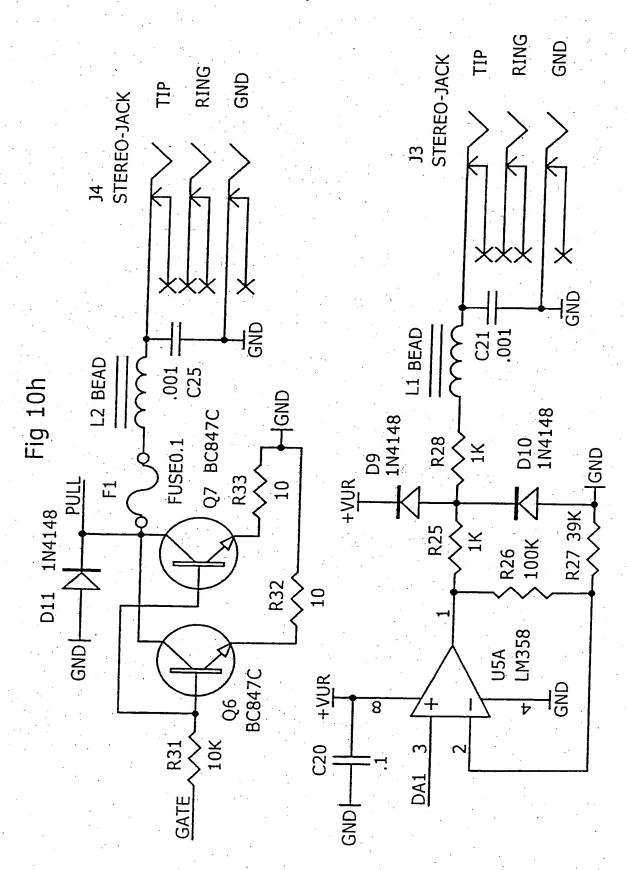
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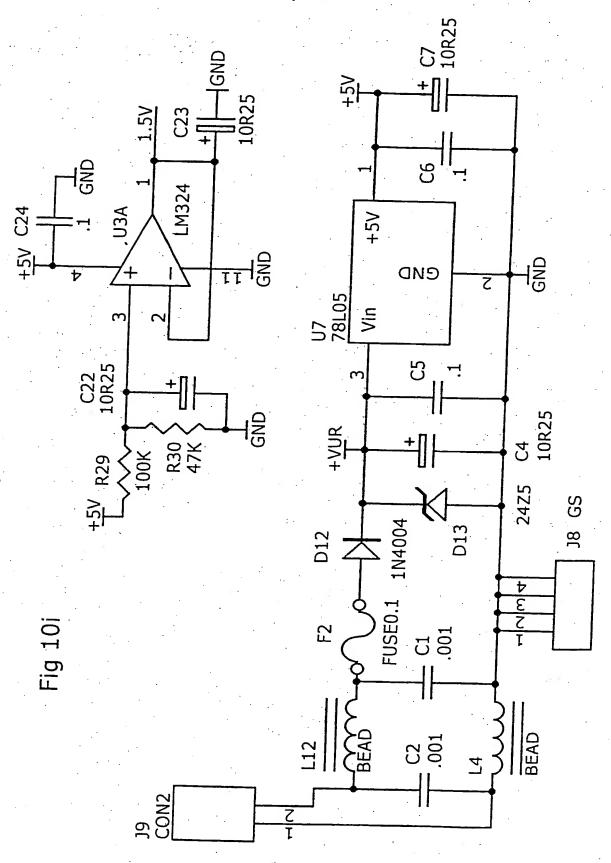
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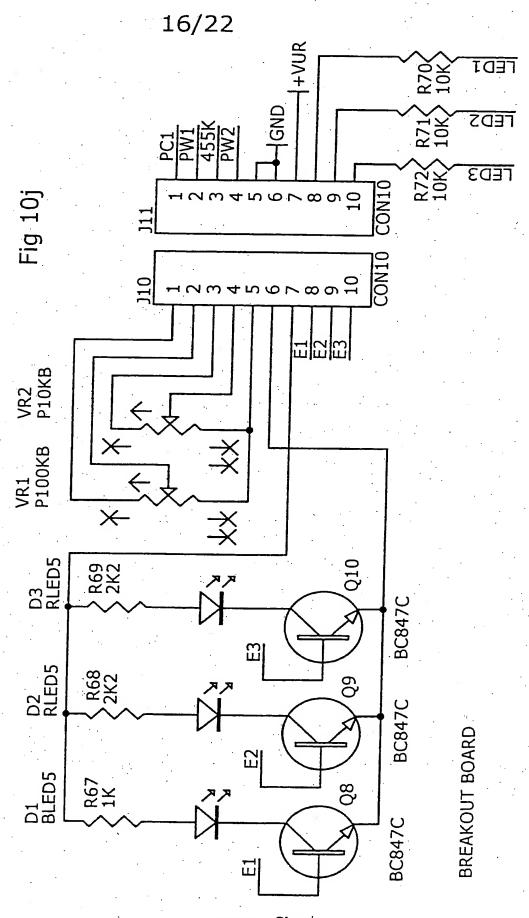
Substitute Sheet (Rule 26) RO/AU

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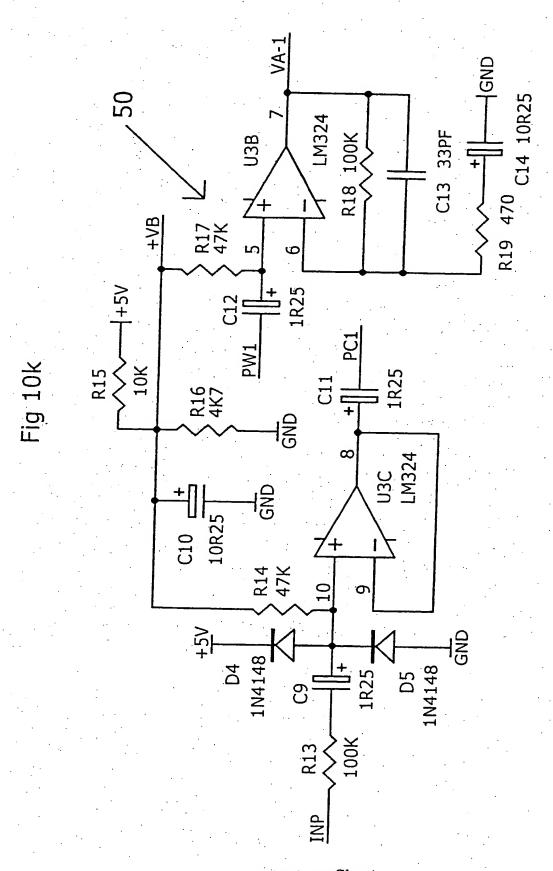
Substitute Sheet (Rule 26) RO/AU



Substitute Sheet (Rule 26) RO/AU

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Substitute Sheet (Rule 26) RO/AU

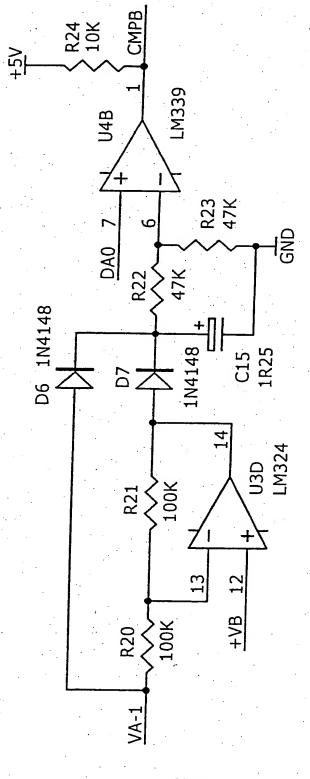
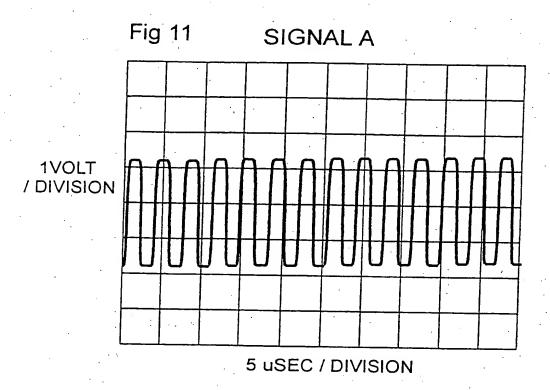
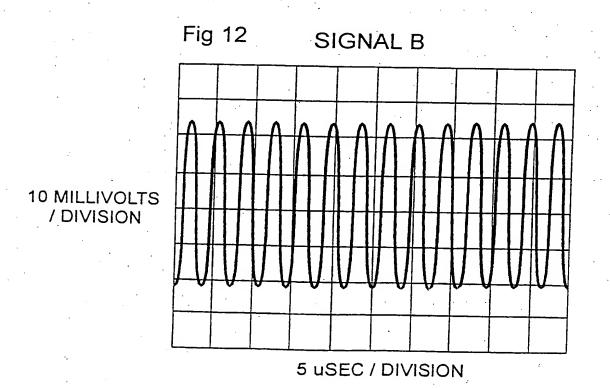


Fig 10L

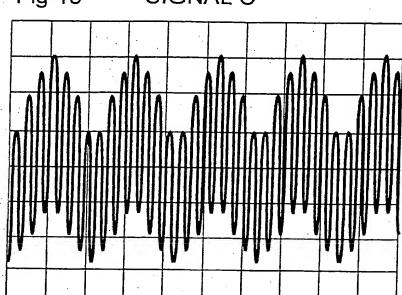
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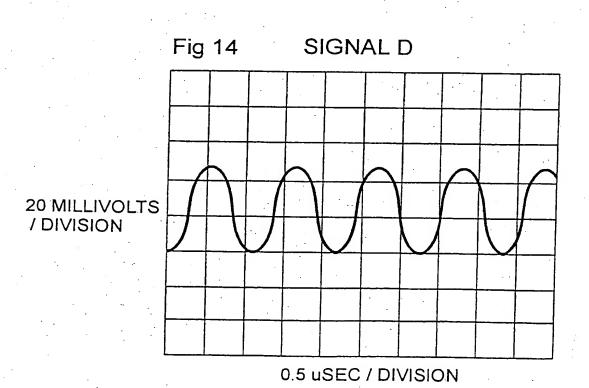
Fig 13 SIGNAL C



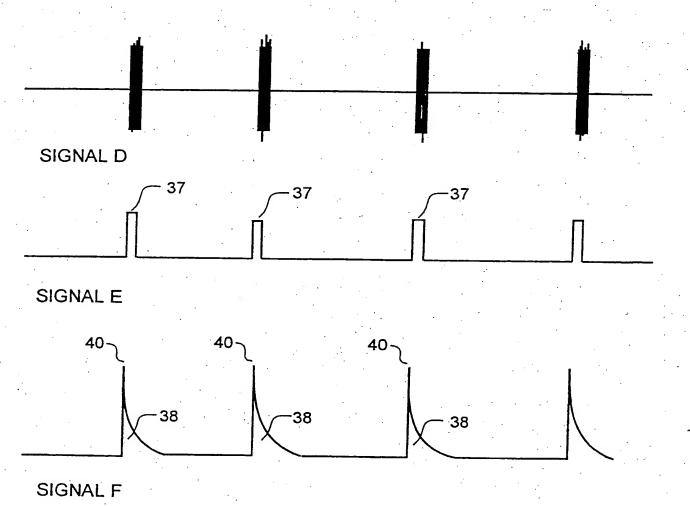
20 MILLIVOLTS / DIVISION ,

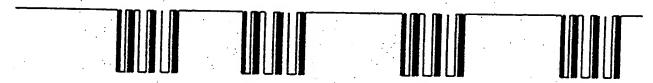
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SIGNAL G

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Fig 15

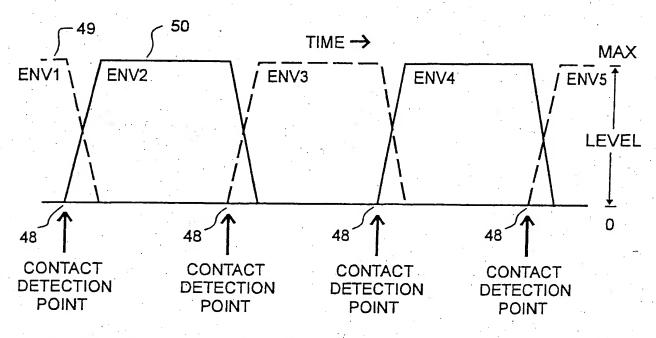


FIG 16

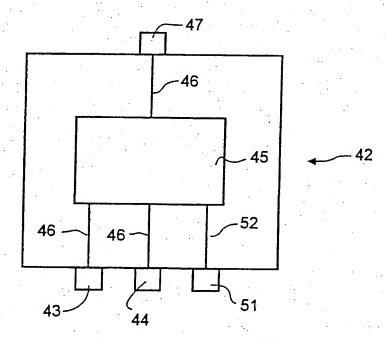


FIG 17